

### **AMENDMENTS TO THE CLAIMS**

1. (Previously Presented) A line-on glass liquid crystal display panel, comprising:  
a picture display part having liquid crystal cells at each intersection of first ~ (n)th gate lines and first ~ (m)th data lines;

first~(m)th data pads extended from the first ~ (m)th data lines in an outer area of the picture display part;

first~(n)th gate pads extended from the first ~ (n)th gate lines in the outer area of the picture display part;

a plurality of first line-on glass signal pads formed just beside the first data pad and a plurality of second line-on glass signal pads formed just beside the first gate pad, the first and second line-on glass signals pads are in one corner of the outer area of the picture display part, wherein the one corner of the outer area of picture display part is defined between the first gate pad and the first data pad;

a plurality of line-on glass type signal lines connecting the first and second line-on glass signal pads in the corner of the outer area of the picture display part for applying gate power voltage signals and gate control signals to gate drive ICs in order to drive gate signal lines of the picture display part;

a plurality of first dummy pads between the first line-on glass type signal pads and a plurality of second dummy pads between the second line-on glass type signal pads; and

a plurality of dummy lines connecting the first and second dummy pads in the one corner of the outer area of the picture display part, wherein the plurality of first and second dummy lines are formed between the line-on glass type signal lines for applying a common voltage as a reference voltage to drive the liquid crystal cells with at least one layer of insulating film therebetween, wherein the insulating film covers the plurality of line-on glass type signal lines and the dummy lines are formed on the layer of insulating film.

Claims 2-4 (Cancelled)

5. (Previously Presented) The line-on glass liquid crystal display panel according to claim 1, wherein the line-on glass type signal lines are formed in a same layer as the gate line of the picture display part.

6. (Previously Presented) The line-on glass liquid crystal display panel according to claim 5, wherein the dummy line is formed in a same layer as the data line of the picture display part with a gate insulating film therebetween.

7. (Withdrawn) The line-on glass liquid crystal display panel according to claim 6, wherein the dummy line is located between the line-on glass type signal lines with the gate insulating film therebetween.

8. (Withdrawn) The line-on glass liquid crystal display panel according to claim 5, wherein the dummy line is formed in a same layer as a pixel electrode of the picture display part.

9. (Withdrawn) The line-on glass liquid crystal display panel according to claim 8, wherein the dummy line is located between the line-on glass type signal lines with a gate insulating film and a protective film therebetween, and the gate insulating film and the protective film are formed to cover the gate line.

Claims 10-11 (Cancelled)

12. (Previously Presented) A fabricating method of a line-on glass liquid crystal display panel, comprising:

forming first~ (n)th gate lines in a picture display part and a plurality of line-on glass signal lines in one corner of an outer area of the picture display part on a substrate for applying gate power voltage signals and gate control signals to gate drive ICs in order to drive gate signal

lines of the picture display part;

forming at least one layer of insulating film to cover the line-on glass type signal lines;

forming first~(m)th data lines to cross the first~(n)th gate lines in a picture display part and a dummy line that is located between the line-on glass signal lines on the insulating film for applying a common voltage as a reference voltage; and

forming first~(m)th data pads extended from the first~(m)th data lines and first~(n)th gate pads extended from the first~(n)th gate lines in the outer of the picture display part and forming first and second line-on glass signal pads just beside the first data pad and first gate pad, respectively and first dummy pads between the first line-on glass signal pads and second dummy pads between the second line-on glass pads, respectively, in one corner of the outer area of the picture display part, wherein the one corner of the outer area of picture display part is defined between the first gate pad and the first data pad,

wherein each of the plurality of the line-on glass signal lines is connected between the first and the second line-on glass signal pads in the one corner of the outer area of the picture display part.

13. (Previously Presented) The fabricating method according to claim 12, further comprising:

forming a gate electrode connected to the gate line of the picture display part on the substrate;

forming a gate insulating film on the substrate on which the gate line and the gate electrode are formed;

forming a semiconductor layer on the gate insulating film;

forming a source electrode connected to the data line, and a drain electrode opposite to the source electrode with a designated gap therebetween, on the substrate on which the semiconductor is formed;

forming a protective film on the substrate where the data line, the source electrode and the drain electrode are formed; and

forming a pixel electrode connected to the drain electrode on the protective film.

14. (Previously Presented) The fabricating method according to claim 12, wherein the line-on glass signal lines are formed of a same metal as the gate line.

15. (Original) The fabricating method according to claim 14, wherein the dummy line is formed of a same metal as the data line.

16. (Original) The fabricating method according to claim 15, wherein the dummy line is formed between the line-on glass type signal lines with the gate insulating film therebetween.

17. (Withdrawn-Previously Presented) The fabricating method according to claim 14, wherein the dummy line is formed of a same metal as the pixel electrode.

18. (Withdrawn) The fabricating method according to claim 17, wherein the dummy line is formed between the line-on glass type signal lines with the gate insulating film and the protective film therebetween.

19. (Previously Presented) A line-on glass liquid crystal display panel, comprising:  
a picture display part with a matrix of liquid crystal cells having a plurality of gate lines and data lines to cross each other;

a gate pad and a data pad to drive the gate lines and data lines, respectively, the gate pad and the data pad are formed in an outer area of the picture display part of a low substrate;

a plurality of line-on glass type signal lines located in one corner of the outer area of the picture display part of the lower substrate for applying drive signals to drive the liquid crystal cells, wherein the one corner of the outer area of the picture display part is corresponding to between the gate pad and the data pad;

an insulating layer covering the line-on glass type signal lines; and

a plurality of common voltage signal lines for applying a common voltage signal and being formed between line-on glass type signal lines, on the insulating layer,

wherein at least one of the plurality of common voltage lines applies the common voltage signal through a silver(Ag) dot to a common electrode that is formed on an entire surface of an upper substrate.

20. (Previously Presented) The line-on glass liquid crystal display panel according to claim 19, wherein the gate signal lines are Vgl, Vcc, Vgh, GOE, GSC, GSP.

21. (Currently Amended) A line-on glass liquid crystal display panel, comprising:  
a picture display part with a matrix of liquid crystal cells having a plurality of gate lines and data lines to cross each other;

a gate pad and a data pad to drive the gate lines and data lines, respectively, the gate pad and the data pad are formed in an outer area of the picture display part of a low substrate;

a plurality of line-on glass type signal lines located in one corner of the outer area of the picture display part of the lower substrate for applying drive gate signals to drive the liquid crystal cells, wherein the one corner is defined between the gate pad and the data pad; and

a common voltage line located in the one corner, wherein the common voltage line is most adjacent to the gate pad and the data pad, an insulating layer covering the line-on glass type signals and the gate lines; and

a plurality of dummy lines that formed between the line-on glass type signal lines on the insulating layer;

wherein the dummy lines and the data lines on the insulating layer and the dummy lines are a common voltage line or a ground voltage line.

22. (Previously Presented) The line-on glass liquid crystal display panel according to claim 21, wherein the gate signal lines are Vgl, Vcc, Vgh, GOE, GSC, GSP.

23. (Previously Presented) The line-on glass liquid crystal display panel according to claim 21, wherein the common voltage line applies the common voltage signal through a silver(Ag) dot to a common electrode that is formed on an entire surface of an upper substrate.